

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/294,341	(	04/20/1999	MASAAKI HIROKI	0756-1964	6027	
31780	7590	01/14/2004		EXAMINI		
ERIC ROB	BINSON		ZAMANI, ALI A			
PMB 955 21010 SOU	THBANK	ST.		ART UNIT	PAPER NUMBER	
POTOMAC FALLS, VA 20165				2674		
				DATE MAILED: 01/14/2004	15	

Please find below and/or attached an Office communication concerning this application or proceeding.

			Application No.		Applicant(s)					
Office Action Summary			09/294,341		HIROKI, MASAAKI					
			Examiner		Art Unit					
			Ali A. Zamani		2674					
Period fo	The MAILING DATE of this community Reply	nication appe	ears on the cover	sheet with the co	orrespondence ad	dress				
THE N - Exter after - If the - If NO - Failui - Any r	ORTENED STATUTORY PERIOD IN MAILING DATE OF THIS COMMUNISHED STATE OF THE OF THIS COMMUNISH OF THE OF TH	IICATION. Is of 37 CFR 1.136 Imunication. ISO) days, a reply vistatutory period will Ity will, by statute, o	6(a). In no event, howe within the statutory mini apply and will expire scause the application to	ver, may a reply be time mum of thirty (30) days SIX (6) MONTHS from to become ABANDONED	ely filed will be considered timel he mailing date of this co					
1)⊠	Responsive to communication(s) fil	ed on <u>11 Apr</u>	<u>ril 2002</u> .							
2a) <u></u> □	This action is <b>FINAL</b> .	2b)⊠ This a	ction is non-final	ı <b>.</b>						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.									
Dispositi	on of Claims									
5)□ 6)⊠ 7)□	<ul> <li>✓ Claim(s) 1 and 3-44 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>☐ Claim(s) is/are allowed.</li> <li>✓ Claim(s) 1 and 3-44 is/are rejected.</li> <li>☐ Claim(s) is/are objected to.</li> <li>☐ Claim(s) are subject to restriction and/or election requirement.</li> </ul>									
Applicati	on Papers									
10)	The specification is objected to by the drawing(s) filed on is/are Applicant may not request that any objected the Replacement drawing sheet(s) including the oath or declaration is objected the specific or declaration is objected to be specific or declaration in the specific or declaration is objected to be specific or declaration in the specific or declaration is objected to be specific or declaration in the specific or declaration is objected to be specific or declaration in the specific or declaration is objected to be specific or declaration in the specific or declaration is objected to be specificated to be specific or declaration in the specific or declaration is objected to be specificated to be spe	e: a) accepection to the degree the correction	pted or b)  objection of the objection of the objection of the on is required if the	in abeyance. See drawing(s) is obje	37 CFR 1.85(a). ected to. See 37 CF	• •				
Priority u	ınder 35 U.S.C. §§ 119 and 120									
12)										
Attachment			_							
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review ( nation Disclosure Statement(s) (PTO-1449)		5) 🔲 (	Notice of Informal Pa	(PTO-413) Paper No(atent Application (PTC					

Application/Control Number: 09/294,341

Art Unit: 2674

## **DETAILED ACTION**

Page 2

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1 and 3-44 are rejected under 35 U.S.C. 103(a) as being unpatentable over Aoki (US Pat. No. 6,011,533) in view of Asada et al. (US pat. No. 5,883,609).

In regard to claims 1, 8, 14, 21, 27 and 33, Aoki discloses a display device comprising: a display panel (100) comprising a switching element for every pixel electrode (114); a scanning line driving circuit (102) for driving scanning lines of liquid crystal panel, a signal line driving circuit (110a and 110b) for driving signal lines of liquid crystal panel, a control circuit (20) for controlling driving liquid crystal panel, a video processing circuit (col. 1, lines 17-29), a circuit for producing a phase difference (32). Aoki fails to expressly teach in a second signal with respect to phase of a first signal which is inputted to the signal driving circuit or to the scanning circuit line driving circuit or to said scanning line driving circuit. However, Asada discloses a liquid crystal display comprising: an active matrix array having switching elements thereof arranged at cross points between scan lines and data lines (Fig. 1). Furthermore Asada discloses a

.

Application/Control Number: 09/294,341 Page 3

Art Unit: 2674

vertical drive circuit comprises of a plurality of cascade half-bit scan circuits (25-1...25-41) and a horizontal circuit comprises a shift circuit (25) composed of a plurality of cascade half-bit scan circuits (See Fig. 15) which the input pulse signal is shifted in synchronism with one of paired dual-phase input clock signals (CLK) wherein the first signal has a reversed phase relation with second signal (see Fig. 6) and is selected as a drive signal for the shifting in either direction, so scan signals (P-1...P-40) are available with a delay equivalent to half a pulse cycle of the selected clock signal (CLK). Asada substantially shows the concept of using a circuit for producing a phase difference in a second signal with respect to a phase of a first signal which is input to the signal line driving circuit or to scanning driving circuit (25-1...25-41) (see Fig. 15) is old. Thus, it would have been obvious to one of ordinary skill in the art to modify Aoki's matrix display of Fig. 1, to adapt Asada's phase signals and scan circuits (25-1...25-41) as configured in Fig. 15 to provide a multi-purpose display device which peripheral drive circuitry is operative with a small number of control signal terminals and an improved cost effect.

In regard to claims 3, 16, 28 and 34, Aoki discloses a display device wherein each of the first signal and second signal is a clock signal (Fig. 3).

In regard to claims 4, 5, 9-12, 15, 17 and 22, Asada discloses an image display device wherein the first signal has a difference rise time period (tr) and a differ signal fall time period (tf) from second signal (see Fig 7, col. 15, lines 42-51).

As to claim 6, Aoki discloses an image display device wherein the circuit (32) for producing phase difference (Fig. 1) and Asad discloses a circuit (25) which the input pulse signal

Application/Control Number: 09/294,341

Page 4

Art Unit: 2674

is shifted in synchronism with one of paired dual-phase input clock signals (CLK), which is selected as a drive signal for the shifting in either direction (Fig. 15).

In regard to claims 7 and 13, 20, 26, 32 and 38, Aoki discloses an image display device which is a projection type display (see Fig. 38) including a transmission type liquid crystal panel and a light source (1102) (col. 20, lines 7-43).

In regard to claims 18, 23-25, 29-31, 35-37 and 39-44, Asada discloses a display device wherein a length of phase difference is at least a signal rise time period (tr) of the first signal or a signal fall time period (tf) of the first signal, and shorter than a half of a signal holding time period (tc) (see Figs 21-24).

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ali Zamani whose telephone number is (703) 308-6414. The examiner can normally be reached on Monday through Friday from 8:00 a.m. to 5:00 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerepe, can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washingto, DC 20231

Application/Control Number: 09/294,341

Art Unit: 2674

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Ali Zamani

January 5, 2004

Page 5